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Facsimile Number: 703-872-9306

Transmission Date:

October 5, 2004

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Kuzmenka, et al.

Docket No.:

INF-129

Serial No:

10/759,103

Art Unit:

2818

Date Filed:

January 20, 2004

Title:

Buffer Amplifier Architecture for Semiconductor Memory Circuits

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- Associate Power of Attorney (1 page)
- Change of Correspondence Address (1 page)
- Information Disclosure Statement (1 page)
- IDS Form PTO/SB/08a (1 page) citing (2) references

Respectfully submitted,

Matalie Surider

Natalie Swider Legal Assistant

Confirmation Respectfully Requested

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Attorney Docket: INF-129

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January 20, 2004

Examiner:

**TBD** 

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2818

For:

Buffer Amplifier Architecture for Semiconductor Memory Circuits

Mail Stop: Amendment Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

### INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08a that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

Respectfully submitted,

10/5/2004

Date

Attorney for Applicants

Reg. No. 35,272

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PTO/SB/08g (08-03)
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Substitute for form 1449A/PTO				Complete If Known		
				Application Number	10/759,103	
	FORMATION			Filing Date	1/20/2004	
STATEMENT BY APPLICANT (Use as many sheets as necessary)				First Named Inventor	Kuzmenka, et al.	
				Art Unit	2818	
(Use as many anotes as nacasamy)				Examiner Name	TBD	
Sheet	1	of	1	Attorney Docket Number	INF-129	

	U.S. PATENT DOCUMENTS						
<b>C</b>	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
Examiner Initials*		Number - Kind Code <sup>2 (# known)</sup>					
	1	US-6,313,674 B1	11-06-2001	Akita, et al.			
	2	US-6,398,768 B2	05-28-2002	Ooishi			
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FOREIGN PATENT DOCUMENTS						
Examiner C		Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	Cite No.1	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)	MM-DD-YYYY	Applicant of Cited Document		T°
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Traislation is attached.
This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1460.

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